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APPLICANTS Norio Hasegawa, Hinode, JAPAN; Katsuya Hayano, Akishima, JAPAN; Shinji Kubo, Fussa, JAPAN; Yasuhiro Koizumi, Sayama, JAPAN; Yasushi Kawai, Kawagoe, JAPAN;				
** CONTINUING DATA ***** NONE <i>IF</i>				
** FOREIGN APPLICATIONS ***** JAPAN P2003-14838 01/23/2003 <i>IF</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/27/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 26	TOTAL CLAIMS 21
Verified and Acknowledged Examiner's Signature <i>SR</i> Initials		INDEPENDENT CLAIMS 4		
ADDRESS 020457				
TITLE Fabrication method of semiconductor integrated circuit device and mask fabrication method				
FILING FEE RECEIVED 874	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	